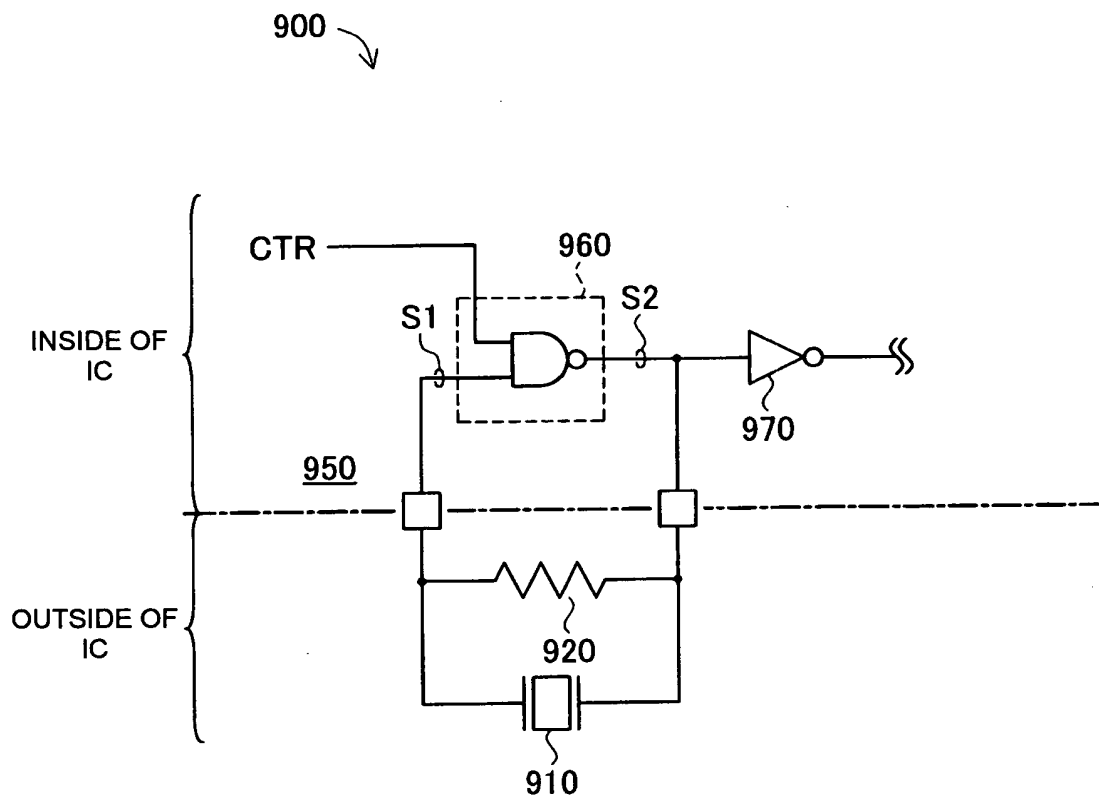


FIG.1



PRIOR ART

FIG.2

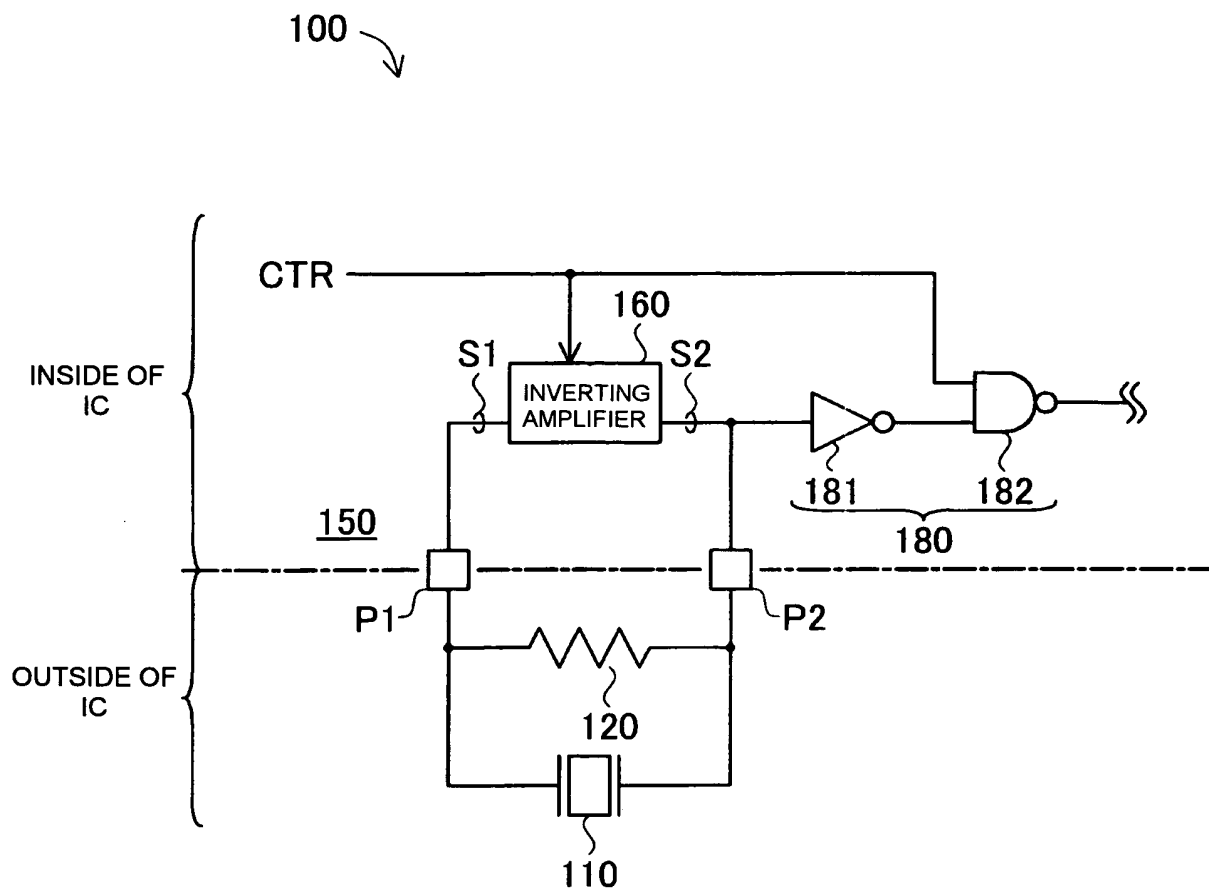


FIG.3 (A)

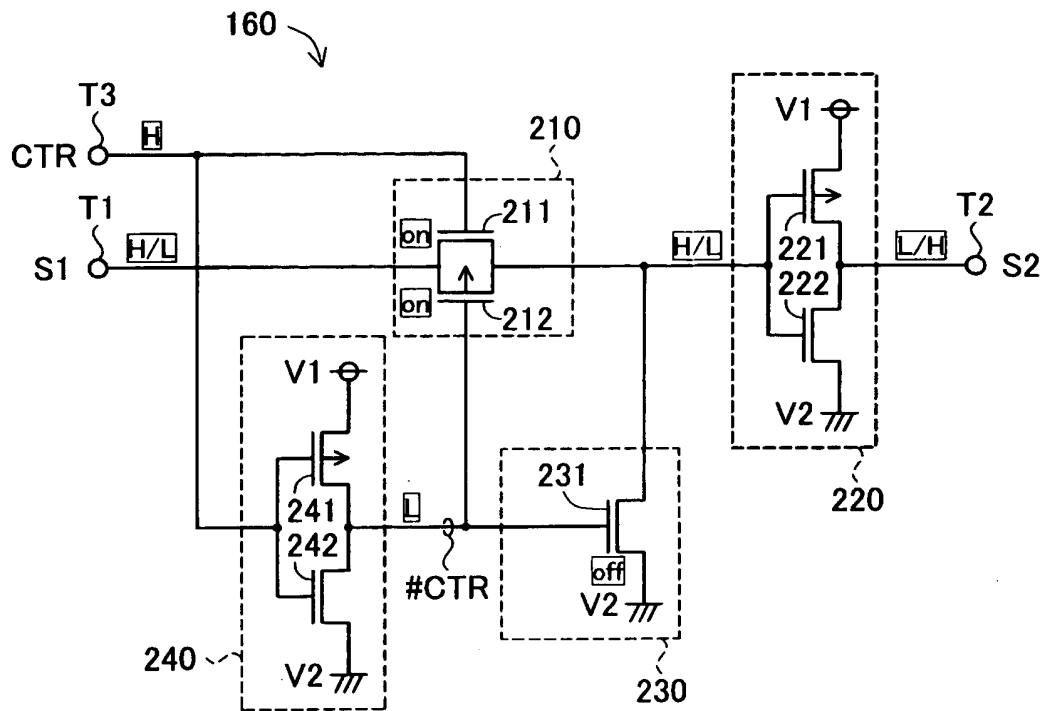


FIG.3 (B)

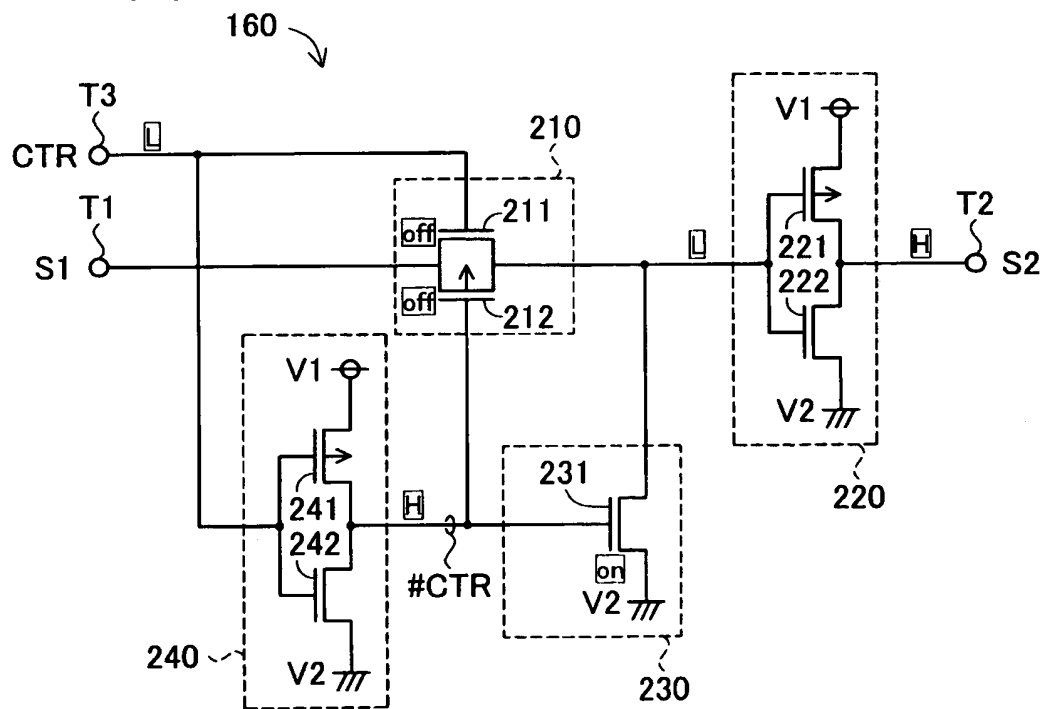
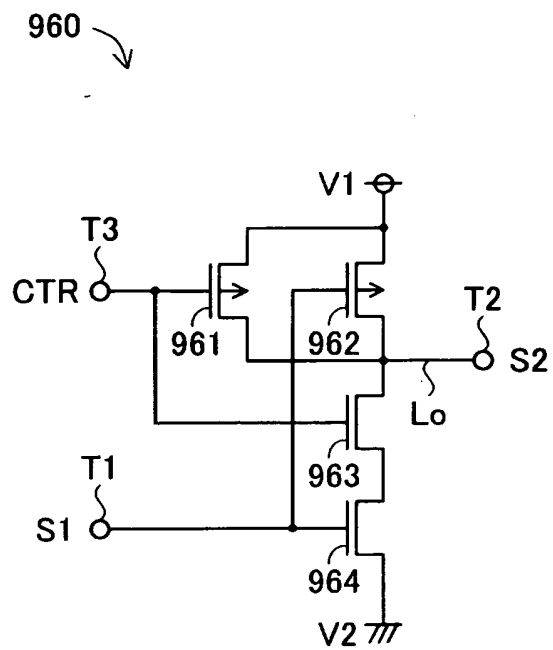


FIG.4



PRIOR ART

The diagram shows a 3-phase inverter 960. It consists of three bridge circuits 961, 962, and 963. Each bridge circuit contains two transistors and two diodes. The inverter is controlled by signals T1, T2, T3, S1, and S2. The DC input is V1 and the common return is V2. The output is connected to a load Lo. The diagram shows the internal structure of the bridge circuits with transistors and diodes.

PRIOR ART

FIG.6

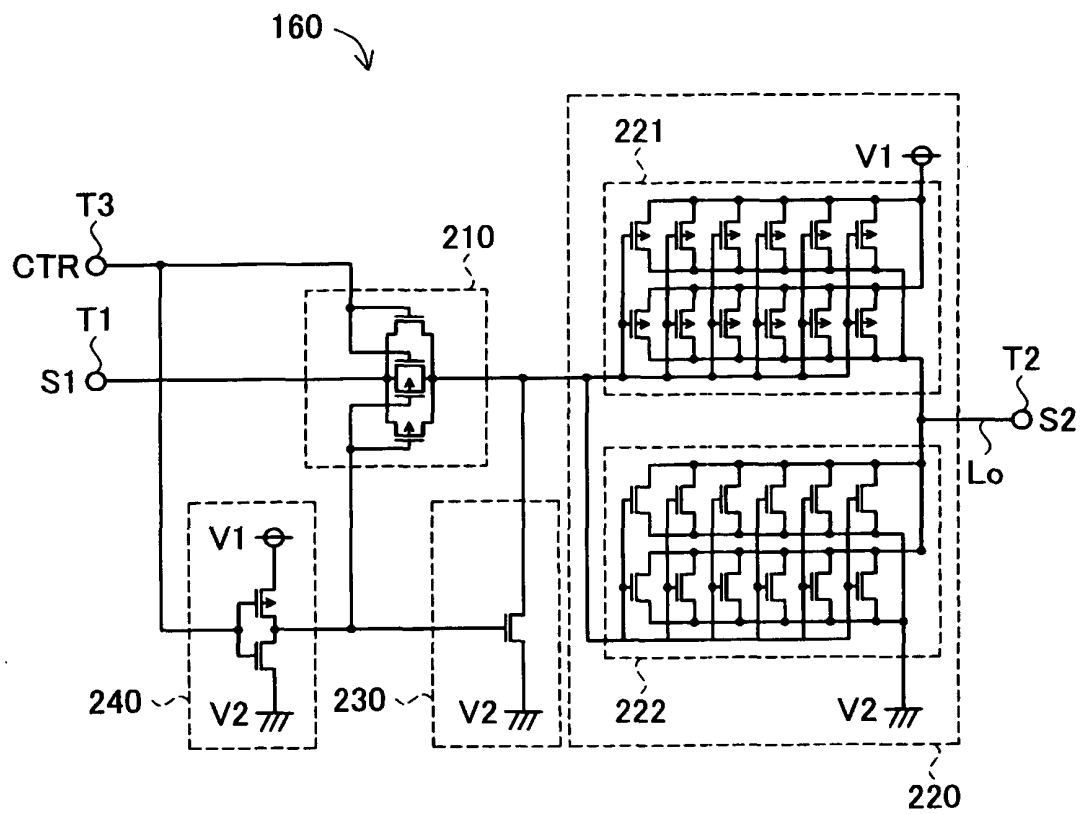


FIG. 7

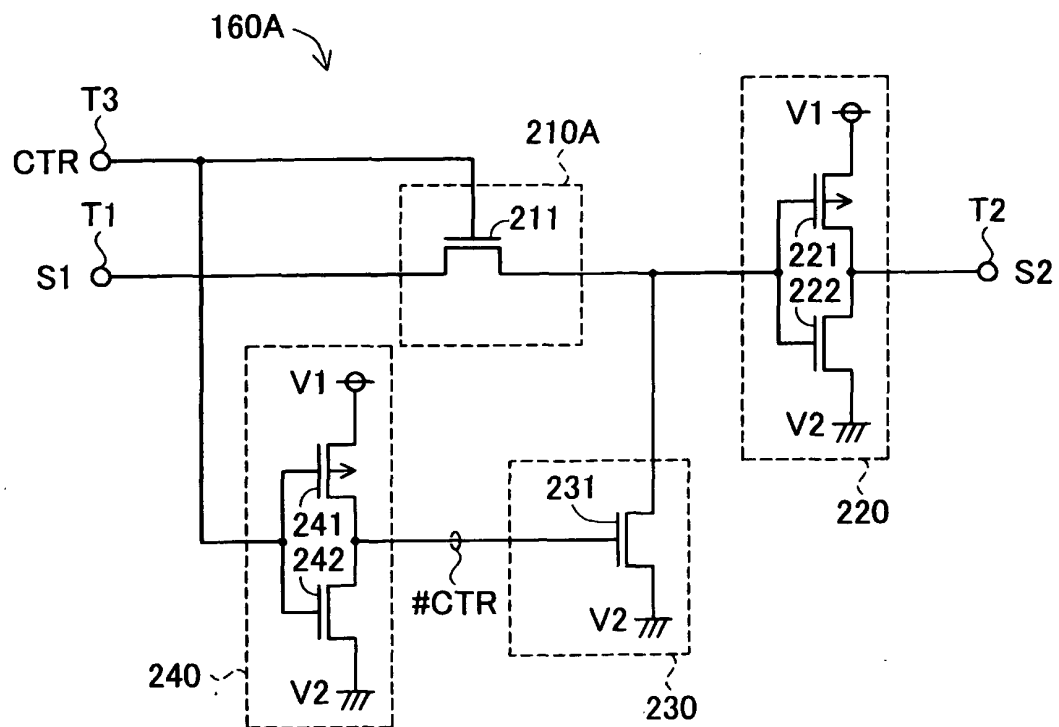


FIG.8

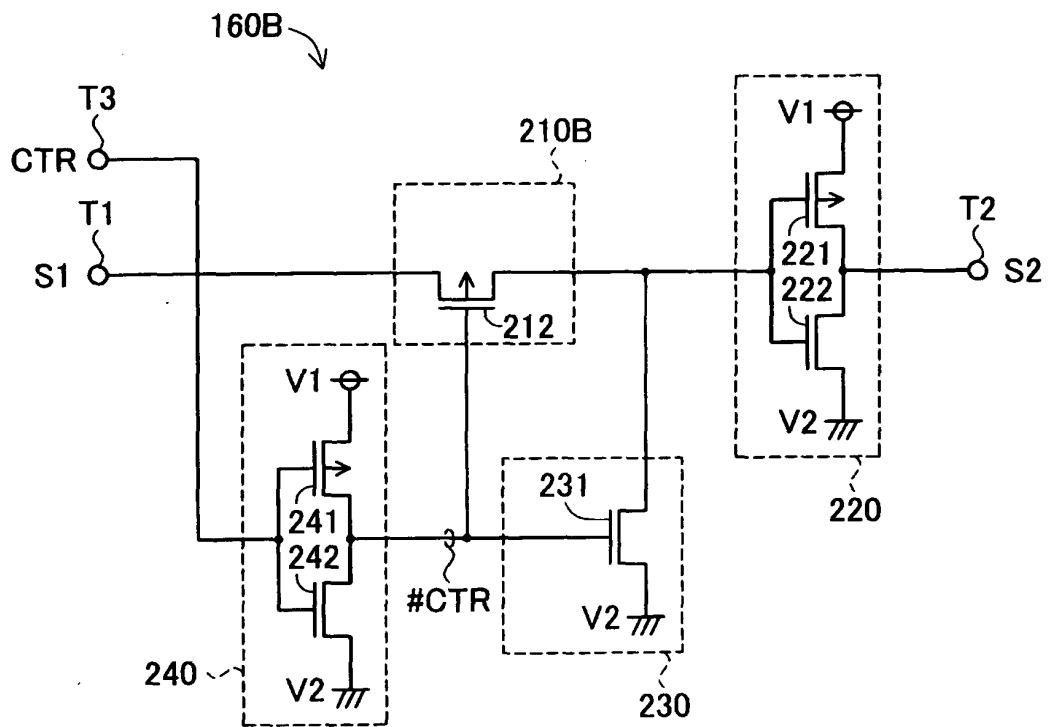


FIG.9

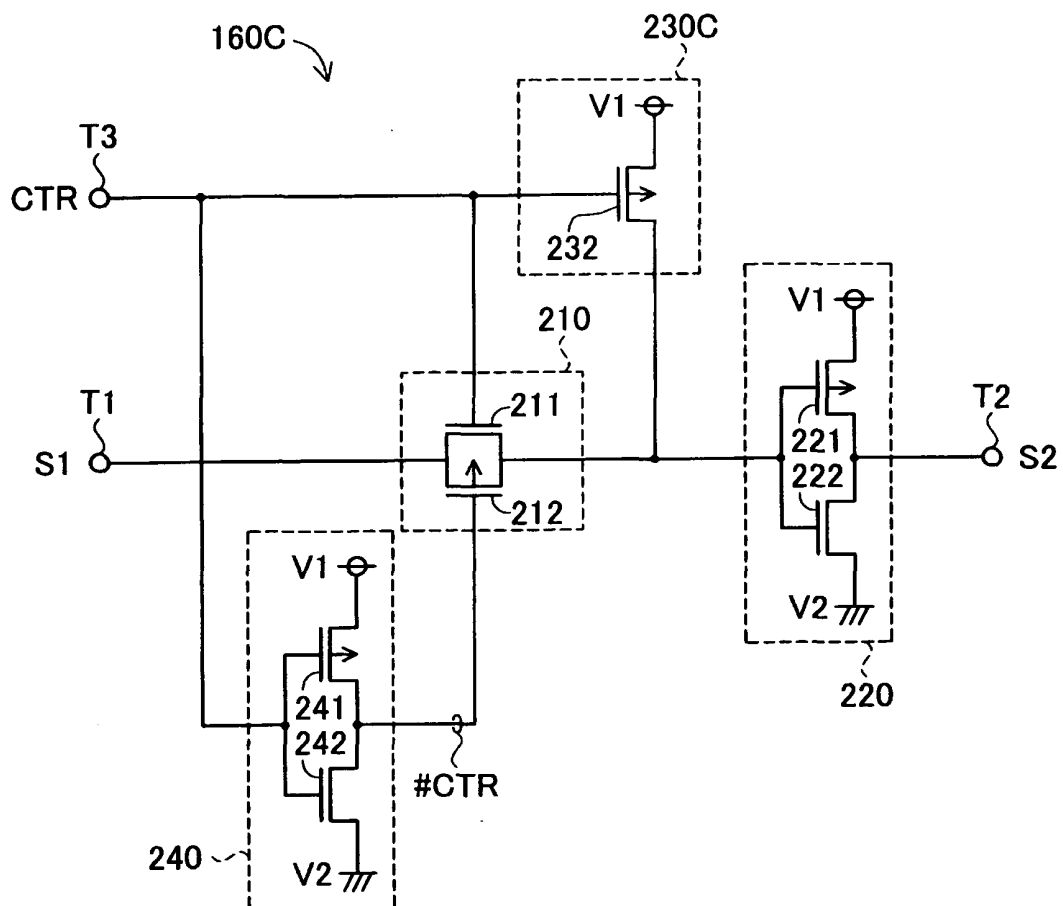


FIG.10

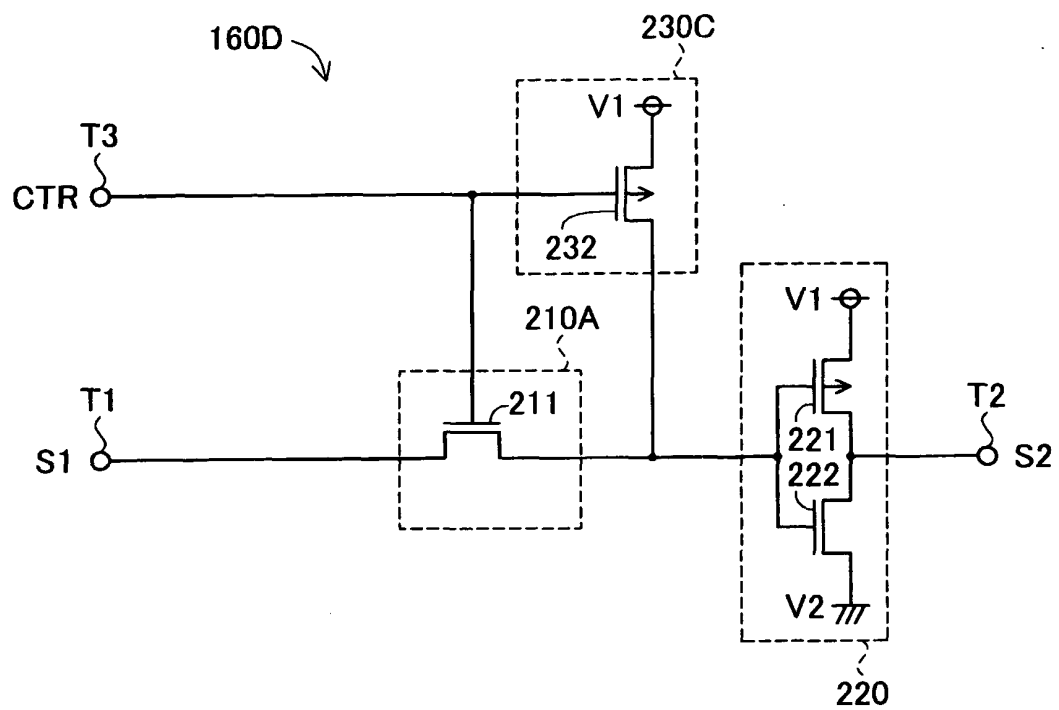


FIG.11

100E

